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	Application No.	Applicant(s)	
Notice of Allowability	09/483,321	SWOBODA, GARY L.	
	Examiner	Art Unit	
	Herng-der Day	2128	
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate comm IGHTS. This application is a and MPEP 1308.	n this application. If not included unication will be mailed in due course.	THIS initiati
 This communication is responsive to <u>Amendment received</u> 	<u>1 //2 1/04</u> .	•	
2. ☑ The allowed claim(s) is/are <u>1-12</u> .	•		
3. $igotimes$ The drawings filed on <u>03 November 2003</u> are accepted by	the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority una) a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application	on No	n the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be subm	MENT of this application.		
INFORMAL PATENT APPLICATION (PTO-152) which give			
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner' Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the second sheet.	son's Patent Drawing Reviews s Amendment / Comment o s84(c)) should be written on t	r in the Office action of he drawings in the front (not the back) o	 f
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 			
Attachment(s)	_		
1. Notice of References Cited (PTO-892)		formal Patent Application (PTO-152)	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413), /Mail Date	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	08), 7. ☐ Examiner's	Amendment/Comment	
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit	8 ⊠ Examiner's	Statement of Reasons for Allowance	
of Biological Material	9. ☐ Other	A	
· ·	3. 🗀 Oulei	JEANA HOMERE PRIMARY EXAMINER	
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DETAILED ACTION

- 1. This communication is in response to Applicant's Amendment to Office Action dated April 22, 2004, faxed July 21, 2004.
- 1-1. Claims 6, 8, and 12 have been amended. Claims 1-12 are pending.
- 1-2. Claims 1-12 have been examined and allowed.

Reasons for Allowance

- 2. The following is an Examiner's statement of reasons for allowance:
- **2-1.** The closest prior art of record discloses:
- (1) A hierarchical test access port architecture for electronic circuits (Bhattacharya, U.S. Patent 6,378,090 B1);
- (2) A method for debugging an integrated circuit using extended operating modes (Swoboda, U.S. Patent 5,828,824); and
- (3) A format of a state inquiry message with synchronizing bits, 1 start bit, and followed by a predetermined number of data bits (Oh et al., U.S. Patent 6,037,868).
- 2-2. Independent claim 1 is directed at a method for emulation communications via a test data input port and boundary-scan architecture. This independent claim identifies the distinct combination of features of "supplying to the test data input port for communication to the boundary-scan architecture a serial signal having a number of bits greater in number than a number of bits of the serial connection of the plurality of registers, each bit of said serial signal having a first logic state" and "following supply of said serial signal, supplying to the test data

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input port for communication to the boundary-scan architecture a single_start bit having a second logic state opposite to said first logic state followed by a predetermined number of data bits" as described in lines 17-30, page 18 of the specification and as shown in Figure 7. The closest prior art does not teach or reasonably suggest using the alternate data transfer protocol for communication to the boundary-scan architecture. Claim 1 and its dependent claims 2-5 are deemed allowable.

- 2-3. Independent claim 6 is directed at a digital electronic module. This independent claim identifies the distinct combination of features of each individual hardware component utilizing the alternate data transfer protocol as described in the specification in line 31 of page 18 through line 17 of page 19 and as shown in Figure 7 and Figure 8. The closest prior art does not teach or reasonably suggest using the alternate data transfer protocol in this specific digital electronic module. Claim 6 and its dependent claims 7-12 are deemed allowable.
- Any comments considered necessary by Applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Herng-der Day whose telephone number is (703) 305-5269. The Examiner can normally be reached on 9:00 - 17:30.

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If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Jean Homere can be reached on (703) 308-6647. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Herng-der Day H.D. September 3, 2004

JEAN R. HOMERE PRIMARY EXAMINER